

09/486561

Class	Subclass
ISSUE CLASSIFICATION	

FILED UNDER 35 U.S.C. 871

PATENT NUMBER

## U.S. UTILITY Patent Application

FF	O.I.R.E. S40	PATENT DATE
SCANNED	Q.A. Sg	

APPLICATION NO. 09/486561	CONT/PRIOR D F	CLASS 257	SUBCLASS 65	ART UNIT 2811 7821	EXAMINER CRWZ
NOBUAKI HASHIMOTO					

**TITLE--**  
SEMICONDUCTOR DEVICE, METHOD OF MANUFACTURE THEREOF, CIRCUIT BOARD,  
AND ELECTRONIC DEVICE

PTO-2040  
12/99

ISSUING CLASSIFICATION					
ORIGINAL		CROSS REFERENCE(S)			
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)		
INTERNATIONAL CLASSIFICATION					
Continued on Issue Slip Inside File Jacket					

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	(Assistant Examiner) _____ (Date) _____			NOTICE OF ALLOWANCE MAILED 1 2	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	(Primary Examiner) _____ (Date) _____			ISSUE FEE Amount Due _____ Date Paid _____	
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